

REMARKS

In response to the above-identified Office Action, the Applicant submits the below remarks and respectfully requests reconsideration thereof, as amended, in light of these remarks.

The Examiner rejected claims 1, 8, 14 and 16 under 35 U.S.C. 102 (b) as being anticipated by U.S. Patent 4,171,467 (hereinafter Evenchik). The Examiner rejected claims 5, 15 and 16 under 35 U.S.C. 103 (a) as being unpatentable by Evenchik in view of U.S. Patent 6,091,779 (hereinafter Griessbach). The Applicant respectfully traverses these rejections for the reasons set out below.

The Applicant contends that the references alone or in combination do not teach or suggest all limitations of claim 1, or the other independent claims of the present application. The Applicant's argument shall be presented with respect to claim 1. However, these comments are applicable to the other independent claims of the present application, and the Examiner is respectfully requested to consider these comments and remarks when reviewing the other independent claims for allowability.

Evenchik does not teach or suggest sending a consecutive second biphasic pulse at a second time instance after a time interval, wherein said first biphasic pulse and the second biphasic pulse are different types, wherein the time interval between said first time instance and said second time instance represents at least a first set of the data bits, said first set of data bits comprising more than one data bit. Evenchik discloses a signal multiplexing circuit which allows use of a single pair of wires to interface a data and voice communication circuit. In Evenchik when a positive lead pulse is detected, the signal appearing during time period indicated as T1 in Figure 7 will comprise the data message, which in the illustrated case is 3 bits long with each bit being separated from the adjacent bit by some time period labeled as T3 (Column 6, lines 18-29). As illustrated in Figure 7, each bipolar pulse represents one bit. There is absolutely no disclosure in Evenchik of the time interval between the first time instance and the second time instance

representing a first set of the data bits, said first set of data bits comprising more than one data bit.

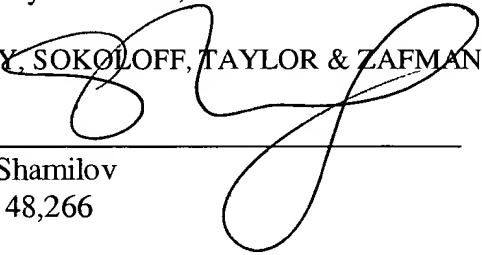
The Applicant submits that the rejections under 35 U.S.C. § 102 (b) and 103 (a) have been addressed, and withdrawal of these rejections is respectfully requested. The Applicant furthermore submits that all pending claims are in condition for allowance, which is earnestly solicited.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due. Furthermore, if an extension is required, then Applicants hereby request such an extension.

Respectfully submitted,

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MARKED UP VERSION OF THE CLAIMS

Please amend the following claim.

1. (Twice Amended) A method for transmitting a sequence of data bits using biphasic pulses, wherein each biphasic pulse comprises a positive pulse and a negative pulse, and wherein biphasic pulses comprises even and odd type, comprising:
 - sending a first biphasic pulse at a first time instance; and
 - sending a consecutive second biphasic pulse at a second time instance after a time interval, wherein said first biphasic pulse and the second biphasic pulse are different types,wherein the time interval between said first time instance and said second time instance represents at least a first set of the data bits, said first set of data bits comprising more than one data bit.